**ECE 324 Homework6: FSM design and simulation**

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| **Exercise** | **Course outcome** | | **Grade** |
| Homework6 | | 2.a, 7.b | /30 |

2.a. Define engineering problems from specified needs for digital systems including implementation on FPGAs using HDL programming.

7.b. Employ appropriate learning strategies such as communicating with an expert, using external resources, experimentation, simulation, etc.